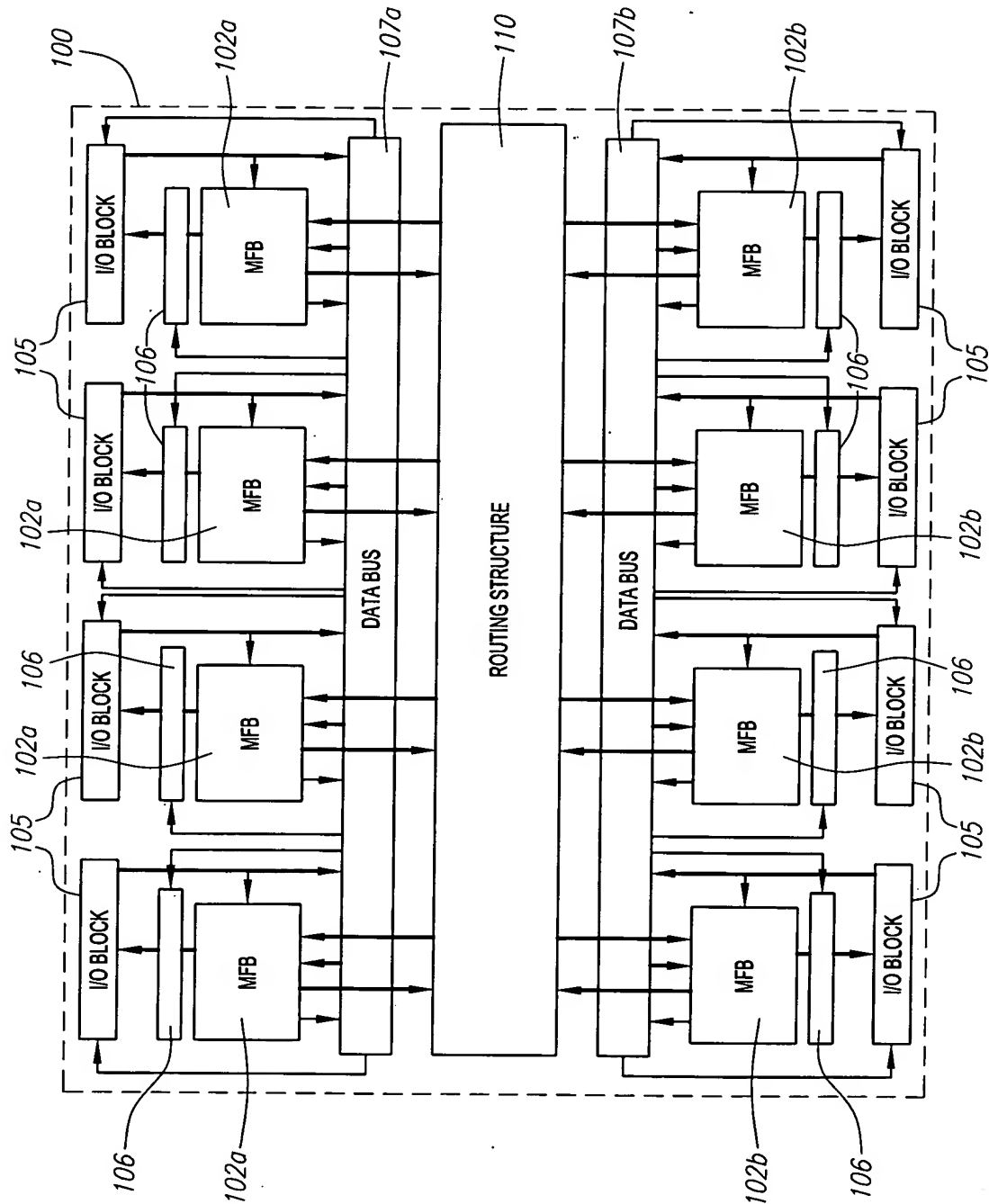


FIG. 1



**FIG. 2A**



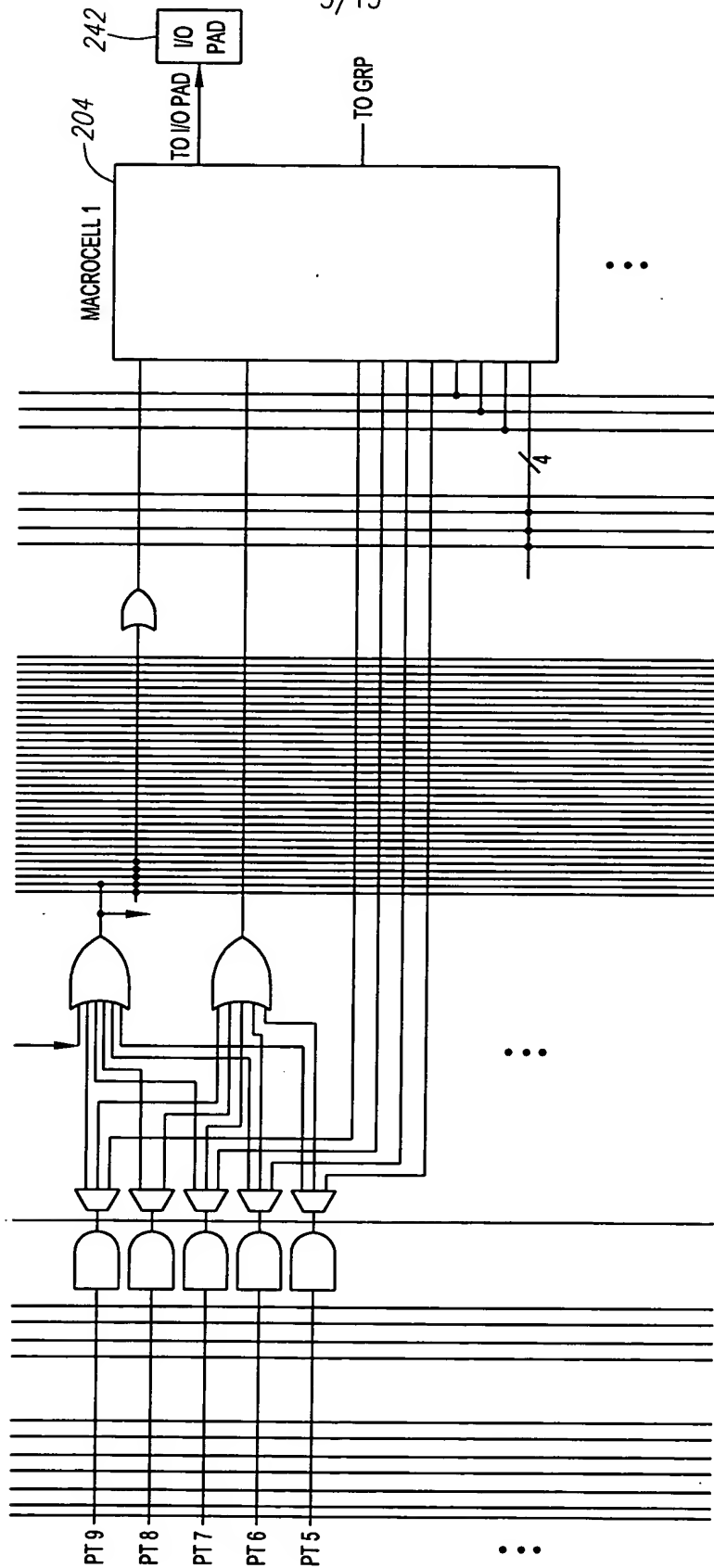


FIG. 2A-2

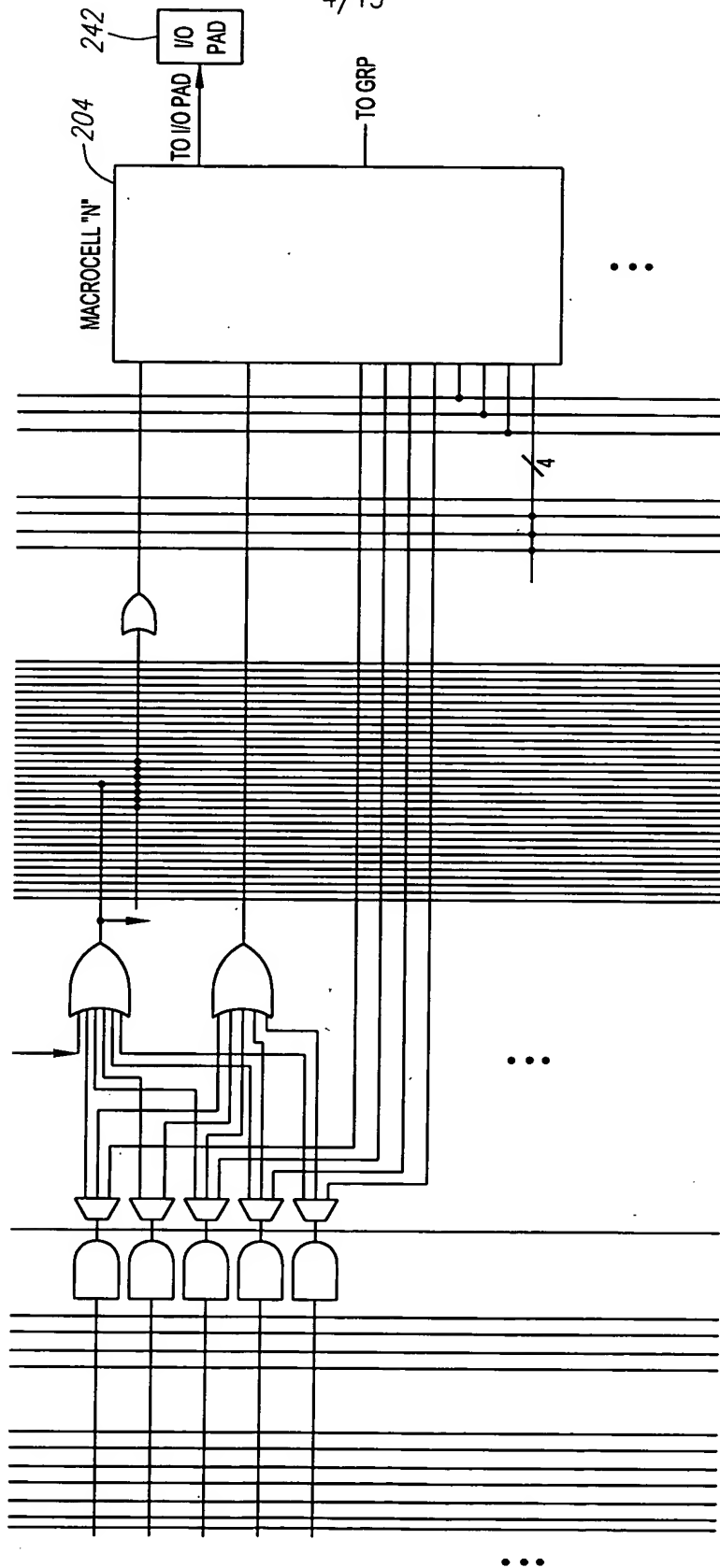
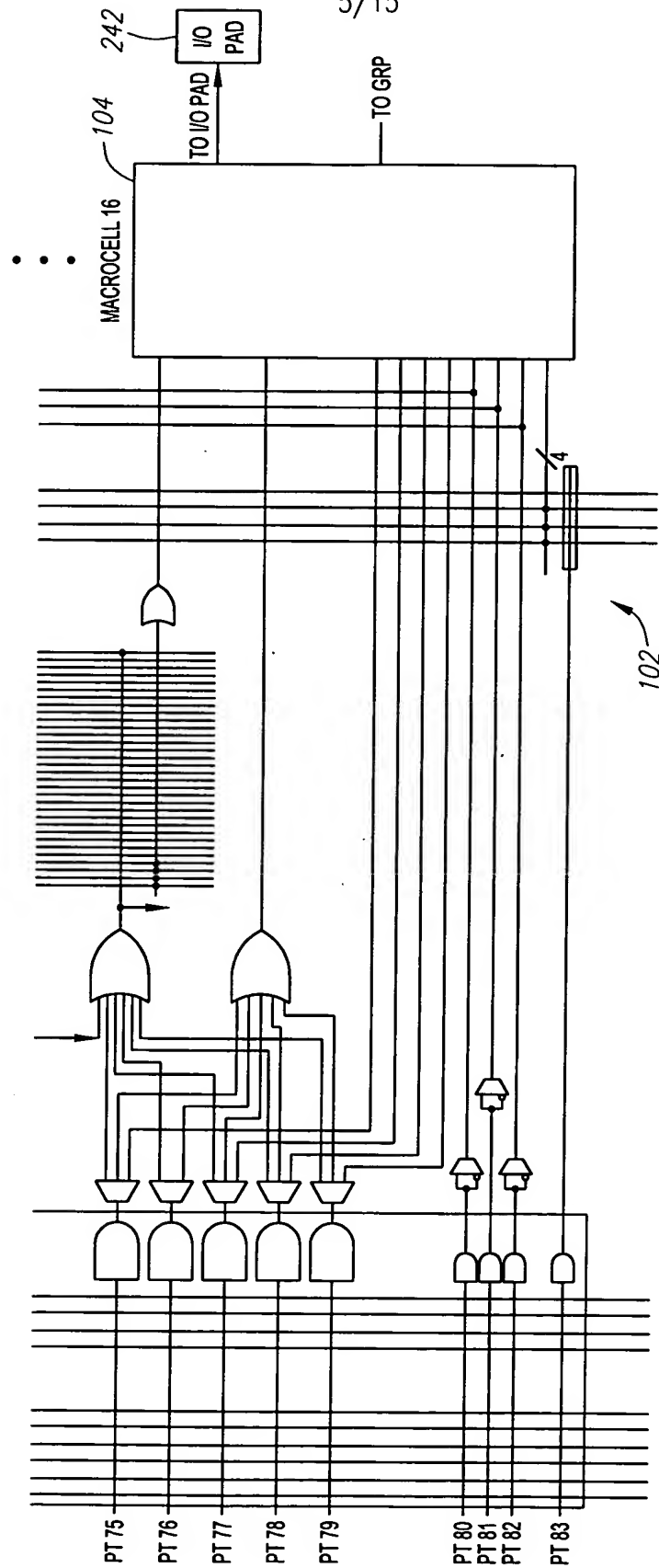


FIG. 2A-3

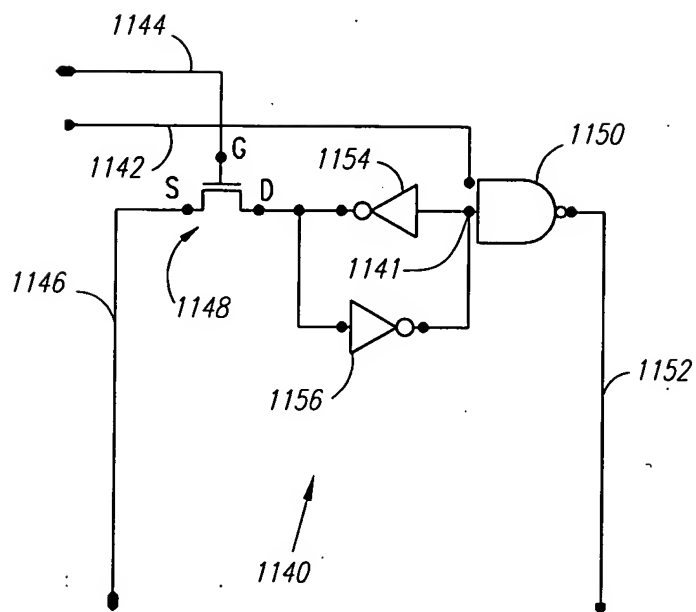


PROGRAMMABLE LOGIC DEVICE WITH ENHANCED  
WIDE INPUT PRODUCT TERM CASCADING

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**FIG. 3**



FIG. 4

PROGRAMMABLE LOGIC DEVICE WITH ENHANCED  
WIDE INPUT PRODUCT TERM CASCADING

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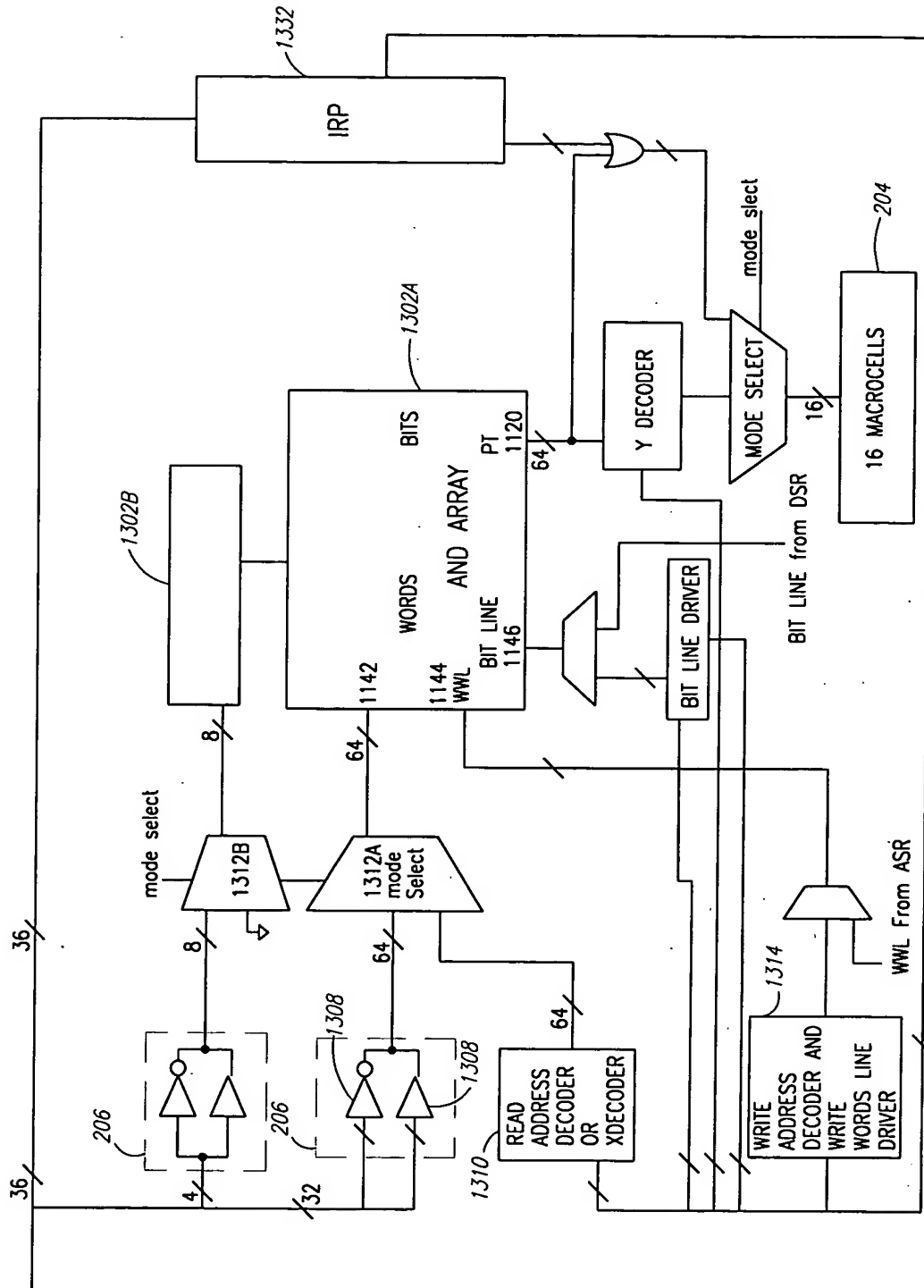


FIG. 5A



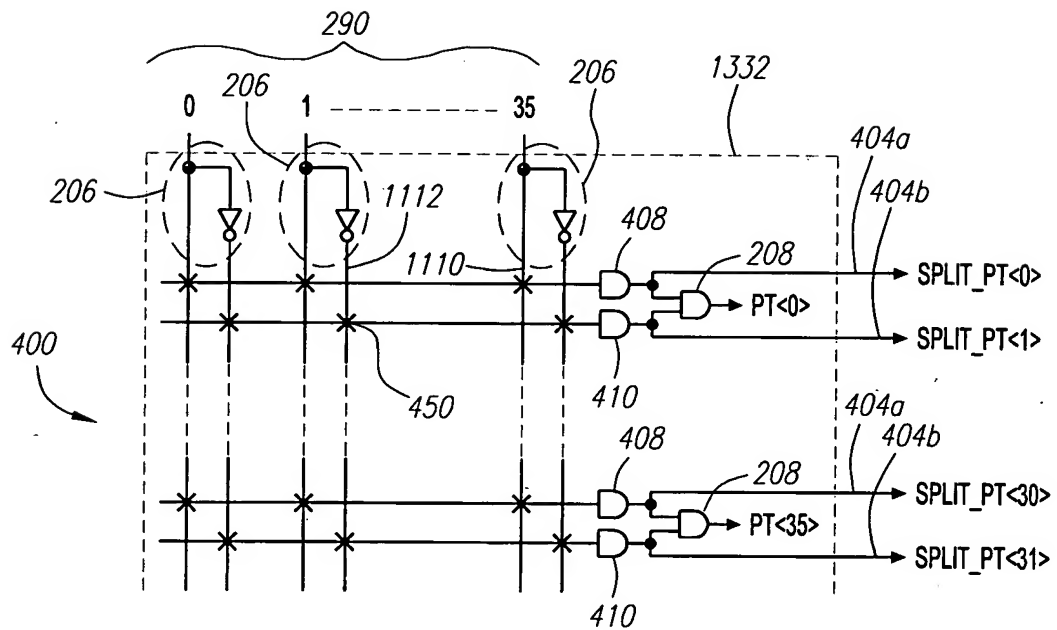


FIG. 5B



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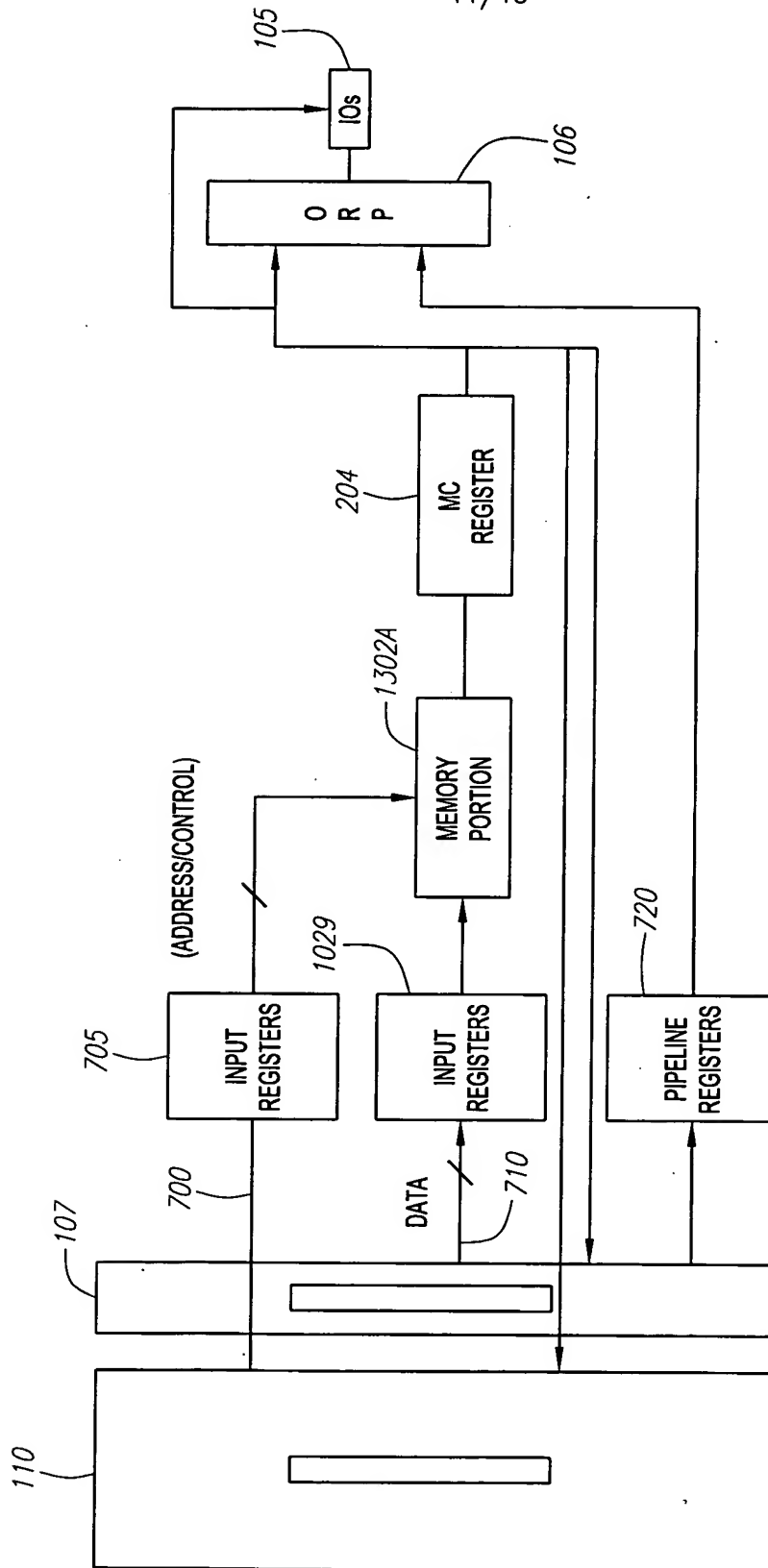


FIG. 7

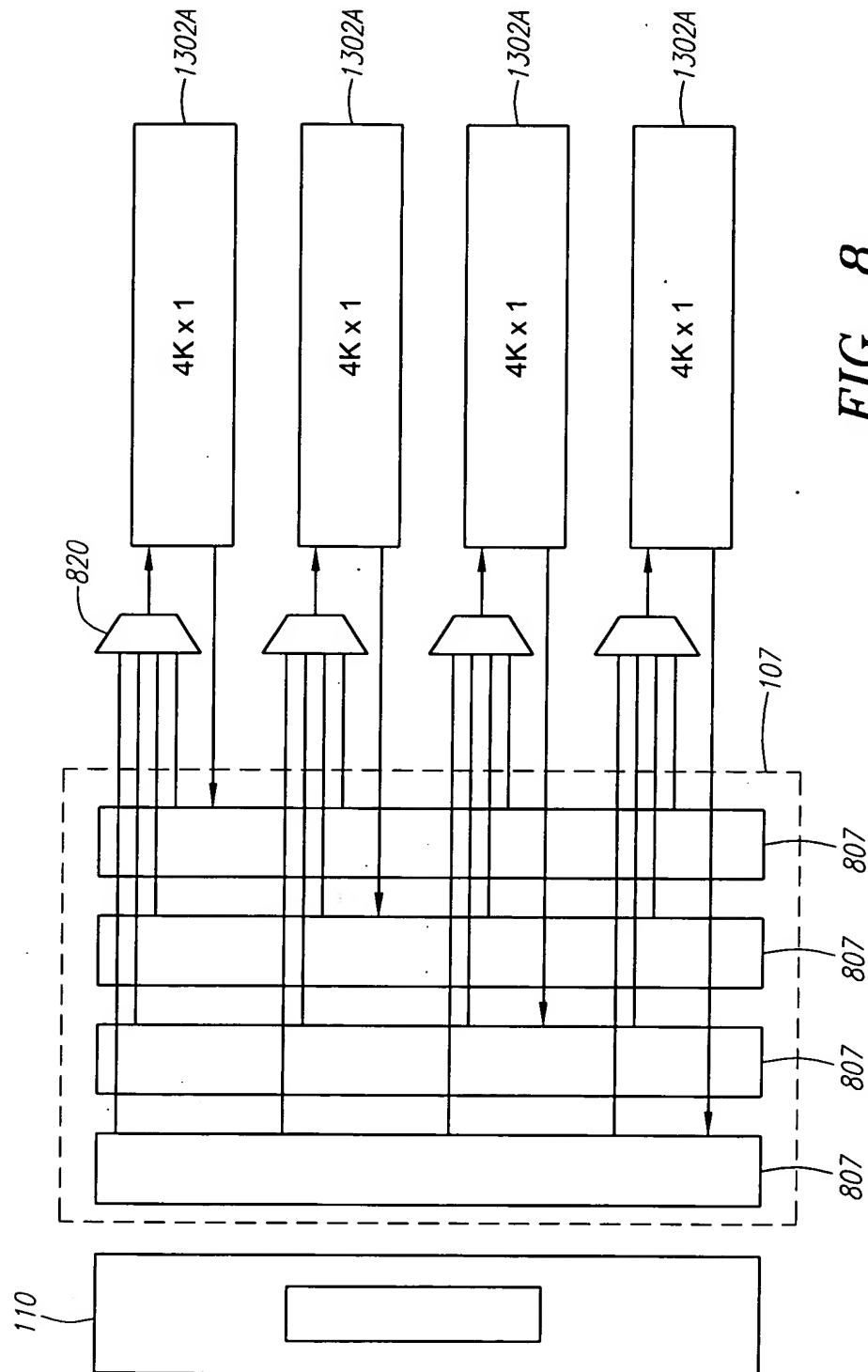


FIG. 8

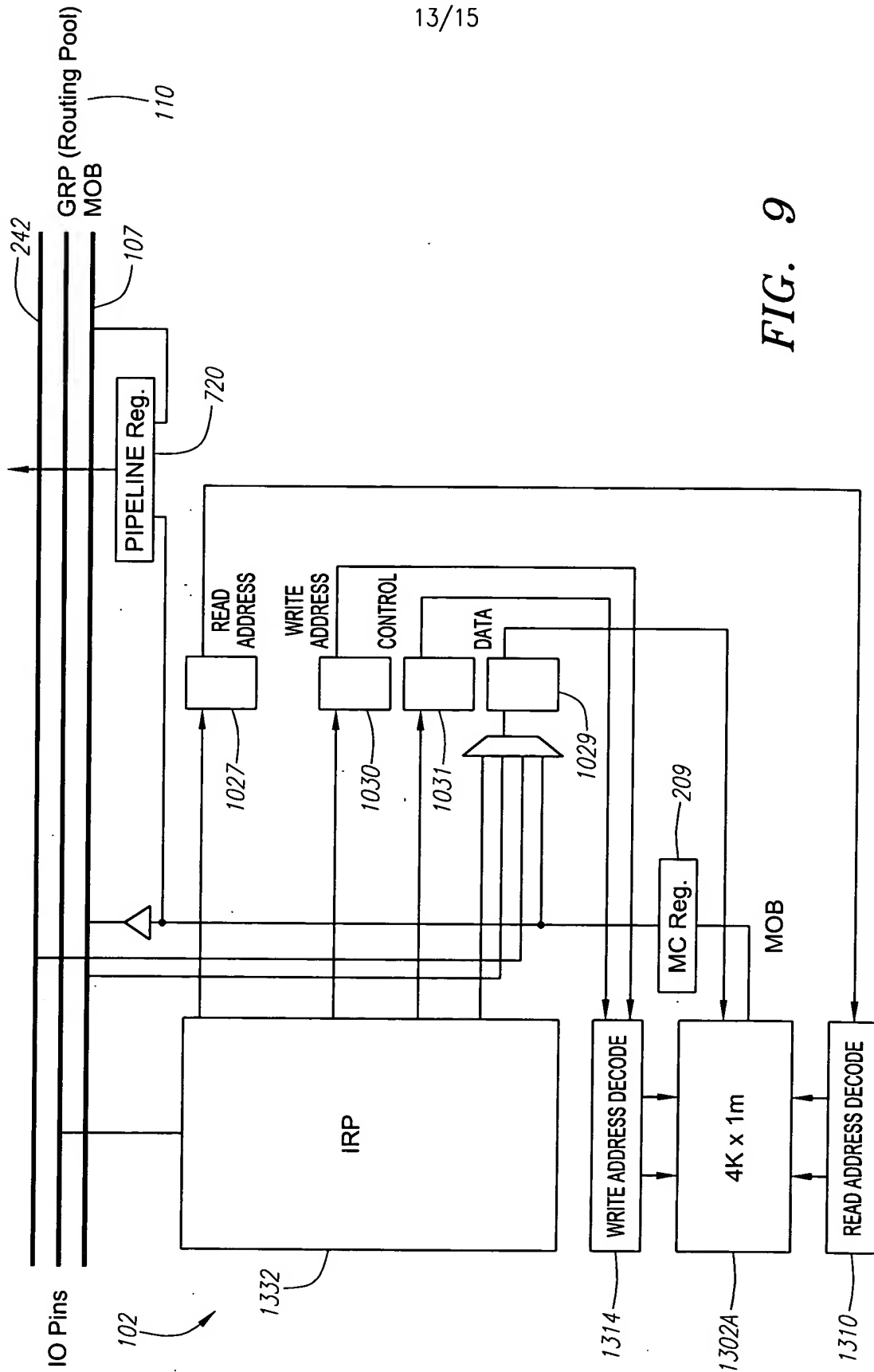
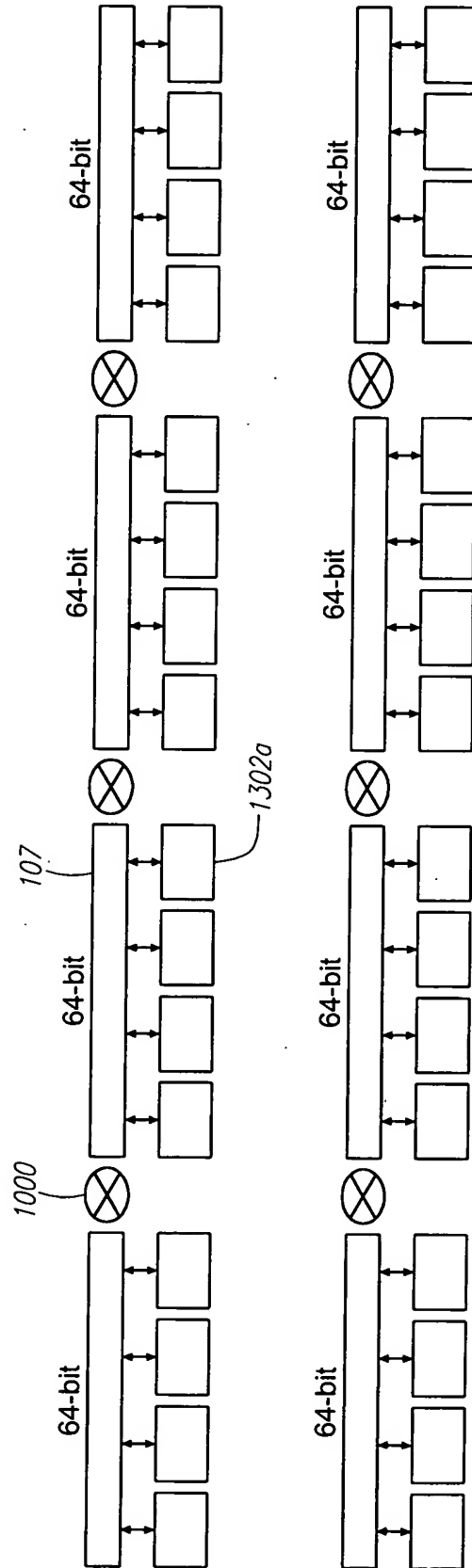


FIG. 9



**FIG. 10**

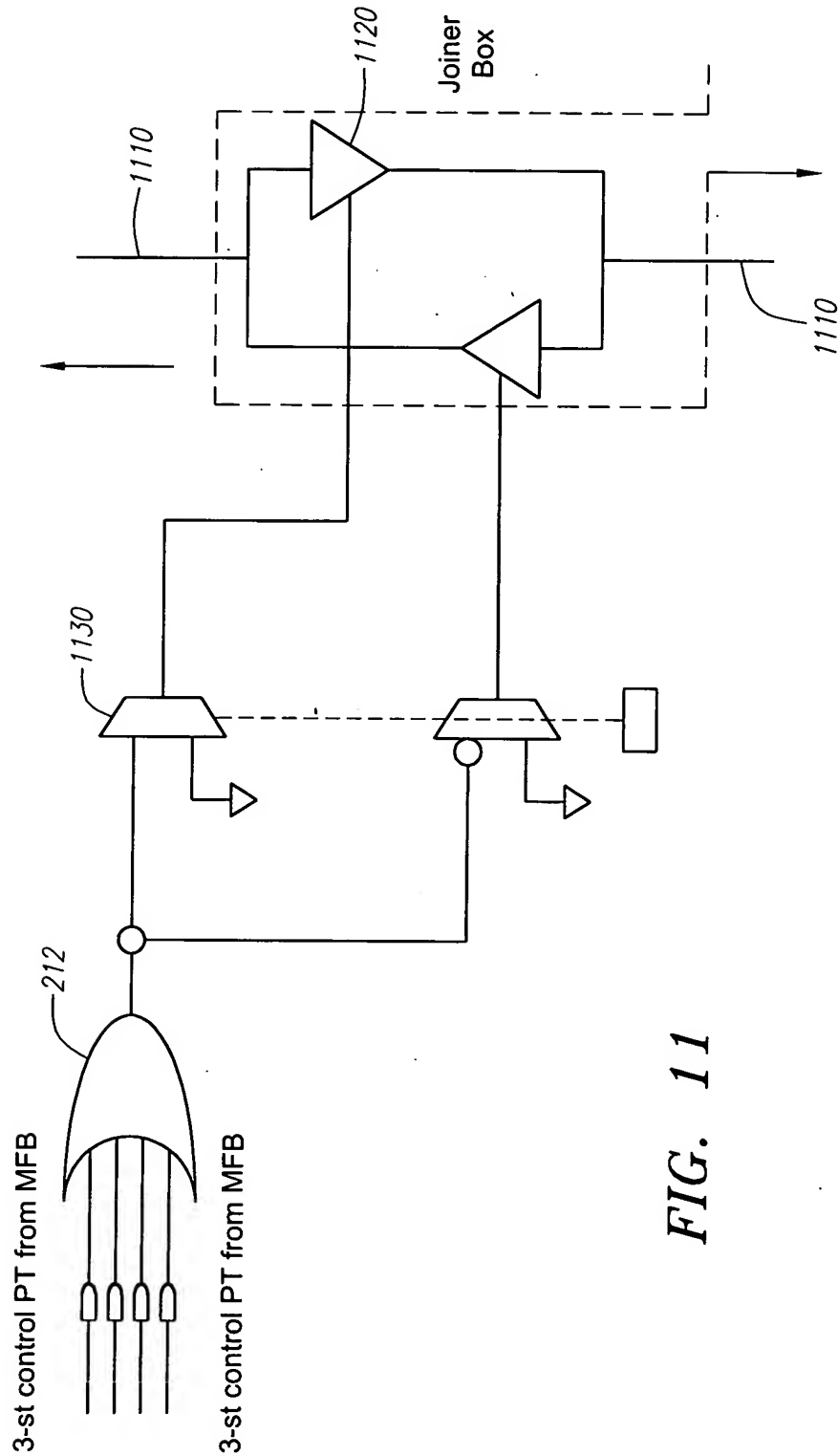


FIG. 11